

# SNS SFP-DGD1-SX Multi-Rate 155Mbps~1.25Gbps SFP 850 nm Multi-Mode Optical Transceiver



#### **Highlights**

- SFP MSA transceiver
- Multi-Rate 155Mbps~1.25Gbps
- Protocols:
- - 1 Gbps Ethernet
- - 1 Gbps Fibre Channel
- Multi-mode fiber
- 850nm VCSEL laser and PIN receiver
- Dual Fiber (Tx/Rx)
- 0 to 500m transmission with 50/125μm MMF
- 0 to 300m transmission with 62.5/125µm MMF
- Duplex LC connector
- Digital Diagnostics
- Hot-swap

#### **Overview**

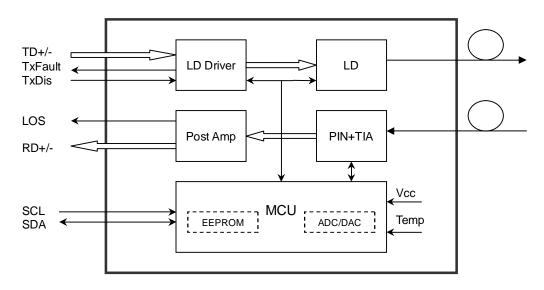
Optical SNS SFP is a high performance transceiver compliant with 1G Small Form-Factor Pluggable (SFP) Multi-Source Agreement (MSA), supporting Multi-Rate 155Mbps~1.25Gbps and transmission distance up to 500m on 50µm MMF. The transceiver module comprises a transmitter with 850nm a vertical cavity surface emitting (VCSEL) laser and a receiver with a PIN photodiode. Transmitter and receiver are separate within a wide temperature range of 0c to +70c and offers optimum heat dissipation and excellent electromagnetic shielding thus enabling high port densities for 1GbE systems.

#### **Specifications**

Data Rates:	155Mbps~1.25Gbps
Wavelength Tx	850 nm
Tx Power	-9.5 ~ -3.0 dBm
Tx Disable	Yes
Wavelength Range	770 - 860 nm
Rx Sensitivity	-18.0 dBm
Rx Overload	-9.9 dBm
Operating Temperature Range	0 to 70°C
Power Consumption	< 1 Watts



The transceivers are compatible with SFP Multi-Source Agreement (MSA) and SFF-8472. For further information, please refer to SFP MSA.



#### **Absolute Maximum Ratings**

**Table 1 - Absolute Maximum Ratings** 

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

#### **Recommended Operating Conditions**

**Table 2 - Recommended Operating Conditions** 

Parameter		Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Standard	Тс	0		+70	°C
Operating case remperature	Extended		-20		+85	°C
Power Supply Voltage		Vcc	3.13	3.3	3.47	V
Power Supply Current		lcc			300	mA
Data Rate				1.25		Gbps



### **Optical and Electrical Characteristics**

SNS SFP-DGD1-SX: (VCSEL and PIN, 500m Reach)

**Table 3 - Optical and Electrical Characteristics** 

Para	meter	Symbol	Min	Typical	Max	Unit	Notes
			Transmi	tter			
Centre V	Vavelength	λс	830	850	860	nm	
Spectral \	Vidth (RMS)	Δλ			0.85	nm	
Average C	Output Power	Pout	-9.5		-3	dBm	1
Extinc	tion Ratio	ER	9			dB	
Optical Rise/Fal	I Time (20%~80%)	tr/tf			0.26	ns	
Data Input S	wing Differential	V <sub>IN</sub>	400		1800	mV	2
Input Differer	ntial Impedance	Z <sub>IN</sub>	90	100	110	Ω	
TX Disable	Disable		2.0		Vcc	V	
I A Disable	Enable		0		0.8	V	
TV Fault	Fault		2.0		Vcc	V	
TX Fault	Normal		0		0.8	V	
			Receive	er			
Centre Wavelength		λс	770		860	nm	
Receive	r Sensitivity				-18	dBm	3
Receiver Overload			-3			dBm	3
LOS De-Assert		LOS <sub>D</sub>			-20	dBm	
LOS Assert		LOSA	-30			dBm	
LOS Hysteresis			1		4	dB	
Data Output Swing Differential		Vout	400		1800	mV	4
	00	High	2.0		Vcc	V	
L	.OS	Low			0.8	V	

- 1. The optical power is launched into MMF.
- PECL input, internally AC-coupled and terminated.
   Measured with a PRBS 2<sup>7</sup>-1 test pattern @1250Mbps, BER ≤1×10<sup>-12</sup>.
- 4. Internally AC-coupled.



## **Timing and Electrical**

**Table 4 - Timing and Electrical** 

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			1	ms
Tx Disable Assert Time	t_off			10	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
LOS Assert Time	t_loss_on			100	μs
LOS De-assert Time	t_loss_off			100	μs
Serial ID Clock Rate	f_serial_clock			400	KHz
MOD_DEF (0:2)-High	V <sub>H</sub>	2		Vcc	V
MOD_DEF (0:2)-Low	VL			0.8	V

#### **Diagnostics**

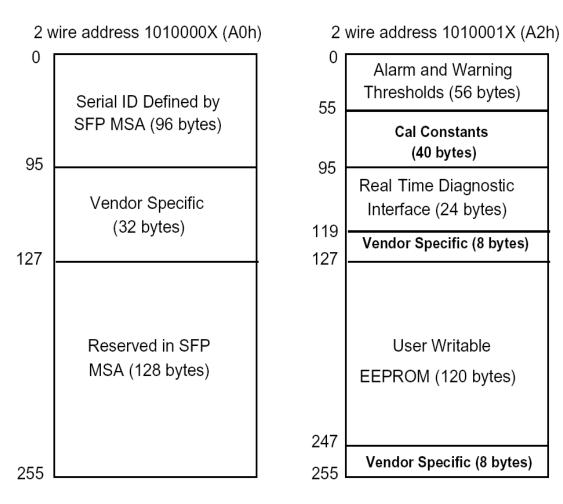
Table 5 - Diagnostics Specification

able 3 – Diagnostics Opecification					
Parameter	Range	Unit	Accuracy	Calibration	
Temperature	0 to +70	°C ±3°C		Internal / External	
remperature	-20 to +85	C	13 0	internar/ Externar	
Voltage	3.0 to 3.6	V	±3%	Internal / External	
Bias Current	0 to 100	mA	±10%	Internal / External	
TX Power	-9.5 to -3	dBm	±3dB	Internal / External	
RX Power	-22 to -3	dBm	±3dB	Internal / External	



#### **Digital Diagnostic Memory Map**

The digital diagnostic memory map specific data field defines as following.





#### **Pin Definitions**

Pin Diagram

20 VeeT	1 VeeT
19 TD-	2 TxFault
18 TD+	3 Tx Disable
17 VeeT	4 MOD-DEF(2)
16 VccT	5 MOD-DEF(1)
15 VccR	6 MOD-DEF(0)
14 VeeR	7 Rate Select
13 RD+	8 LOS
12 RD-	9 VeeR
11 VeeR	10 VeeR
Top of Board	Bottom of Board (as viewed thru top of board)



#### **Pin Descriptions**

Pin	Signal Name	Description	Plug Seq.	Notes
1	V <sub>EET</sub>	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	MOD_DEF(2)	SDA Serial Data Signal	3	Note 3
5	MOD_DEF(1)	SCL Serial Clock Signal	3	Note 3
6	MOD_DEF(0)	TTL Low	3	Note 3
7	Rate Select	Not Connected	3	
8	LOS	Loss of Signal	3	Note 4
9	V <sub>EER</sub>	Receiver ground	1	
10	V <sub>EER</sub>	Receiver ground	1	
11	V <sub>EER</sub>	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 5
13	RD+	Received Data Out	3	Note 5
14	V <sub>EER</sub>	Receiver ground	1	
15	V <sub>CCR</sub>	Receiver Power Supply	2	
16	V <sub>CCT</sub>	Transmitter Power Supply	2	
17	V <sub>EET</sub>	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 6
19	TD-	Inv. Transmit Data In	3	Note 6
20	$V_{EET}$	Transmitter Ground	1	

#### Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

- TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) TX Disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7k~10kΩ resistor. Its states are:

Low (0 to 0.8V): Transmitter on (>0.8V, < 2.0V): Undefined

High (2.0 to 3.465V): Transmitter Disabled Open: Transmitter Disabled

- 3) Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7k~10kΩ resistor on the host board. The pull-up voltage shall be VccT or VccR.
  - Mod-Def 0 is grounded by the module to indicate that the module is present

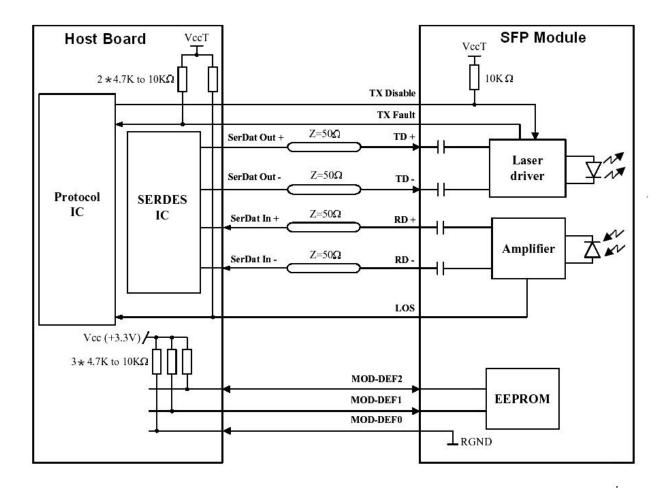
Mod-Def 1 is the clock line of two wire serial interface for serial ID

Mod-Def 2 is the data line of two wire serial interface for serial ID

- 4) LOS is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor. Pull up voltage between 2.0V and Vcc+0.3V. Logic 1 indicates loss of signal; Logic 0 indicates normal operation. In the low state, the output will be pulled to less than 0.8V.
- 5) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with  $100\Omega$  (differential) at the user SERDES.
- 6) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

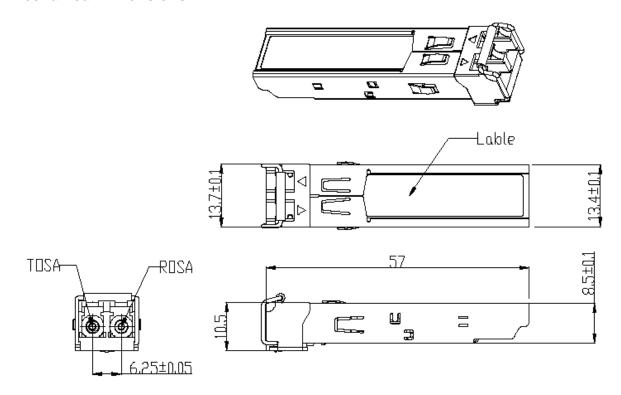


#### **Recommended Interface Circuit**





#### **Mechanical Dimensions**



# **Ordering information**

Part Number	Product Description
SNS SFP-DGD1-SX	Multi-Rate 155Mbps~1.25Gbps SFP 850 nm Multi-Mode Optical Transceiver