

SNS SFP-GDCWXD-XX* 1.25 Gbps 80 km CWDM SFP Transceiver



Highlights

- SFP MSA transceiver
- Data-rate of 1.25Gbps
- Protocols:
- 1 Gbps Ethernet
- 1 Gbps Fibre Channel
- SONET OC-24-LR-1
- · Single-mode fiber
- Dual Fiber (Tx/Rx)
- 9 CWDM DFB wavelengths laser and PIN photodetector
- 10-80km transmission
- Duplex LC connector
- Digital Diagnostics
- Hot-swap

Overview

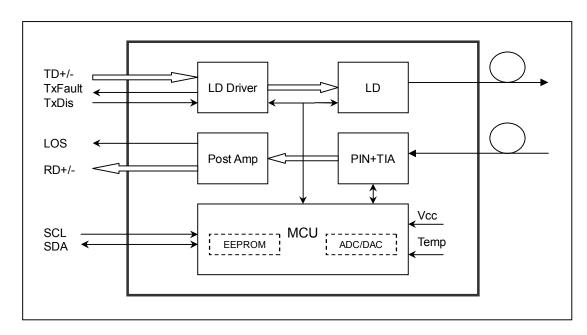
Optical SNS SFP is a high performance transceiver compliant with 1G Small Form-Factor Pluggable (SFP) Multi-Source Agreement (MSA), supporting data-rate of 1.25Gbps and transmission distance up to 80km on SMF. The transceiver consists of three sections: an uncooled CWDM DFB laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. Transmitter and receiver are separate within a wide temperature range of 0C to +70C and offers optimum heat dissipation and excellent electromagnetic shielding thus enabling high port densities for 1GbE systems.

Specifications

1.25Gbps
λc CWDM wavelength
0 ~ 5 dBm
Yes
λc-6.5 : λc+6.5 nm
-23.0 dBm
-3 dBm
0 to 70°C
< 1 Watts



Module Block Diagram



Absolute Maximum Ratings

Table 1 - Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

Recommended Operating Conditions

Table 2 - Recommended Operating Conditions

Parameter		Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Standard	Тс	0		+70	°C
Power Supply Voltage	•	Vcc	3.13	3.3	3.47	V
Power Supply Curren	t	lcc			300	mA
Data Rate				1.25		Gbps



SNS SFP-GDCWXD-xx See table3 below for "xx" values

Table3 -λC Wavelength Guide

	λC Wavelength Guide					
Code	λc	Unit	Code	λς	Unit	
45	1450	nm	55	1550	nm	
47	1470	nm	57	1570	nm	
49	1490	nm	59	1590	nm	
51	1510	nm	61	1610	nm	
53	1530	nm				

Optical and Electrical Characteristics

(CWDM and PIN, 80km Reach)

Table 4 - Optical and Electrical Characteristics

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Para	meter	Symbol	Min	Typical	Max	Unit	Notes
			Transmi	tter			
Centre \	Wavelength	λς	λc-6.5	λς	λc+6.5	nm	
Spectral \	Width (-20dB)	Δλ			1	nm	
Side Mode S	uppression Ratio	SMSR	30			dB	
Average (Output Power	Pout	0		5	dBm	1
Extino	tion Ratio	ER	9			dB	
Optical Rise/Fall Time (20%~80%)		tr/tf			180	ps	
Data Input S	wing Differential	V _{IN}	400		1800	mV	2
Input Differe	ntial Impedance	Z_{IN}	90	100	110	Ω	
TV Diochlo	Disable		2.0		Vcc	V	
TX Disable	Enable		0		0.8	V	
TV Fault	Fault		2.0		Vcc	٧	
TX Fault	Normal		0		0.8	V	
	Receiver						
Receive	r Sensitivity				-23	dBm	3



Receiver Overload		-3		dBm	3
LOS De-Assert	LOS_D		-24	dBm	
LOS Assert	LOSA	-35		dBm	
LOS Hysteresis		1	4	dB	
Data Output Swing Differential	Vout	370	1800	mV	4
LOS	High	2.0	Vcc	V	
LOS	Low		0.8	V	

Notes:

- The optical power is launched into SMF.
 PECL input, internally AC-coupled and terminated.
 Measured with a PRBS 2⁷-1 test pattern @1250Mbps, BER ≤1×10⁻¹².
- 4. Internally AC-coupled.

Timing and Electrical

Table 5 - Timing and Electrical

Parameter Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			1	ms
Tx Disable Assert Time	t_off			10	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
LOS Assert Time	t_loss_on			100	μs
LOS De-assert Time	t_loss_off			100	μs
Serial ID Clock Rate	f_serial_clock			400	KHz
MOD_DEF (0:2)-High	V_{H}	2		Vcc	V
MOD_DEF (0:2)-Low	V_L			8.0	V



Diagnostics

Table 5 – Diagnostics Specification

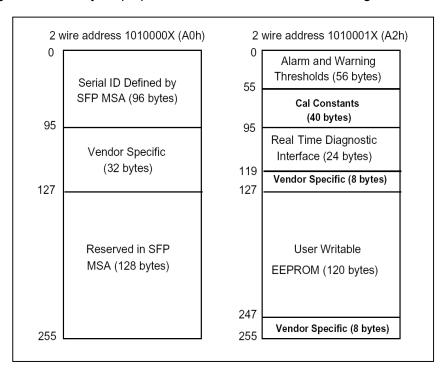
Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal / External
Voltage	3.0 to 3.6	V	±3%	Internal / External
Bias Current	0 to 100	mA	±10%	Internal / External
TX Power	0 to +5	dBm	±3dB	Internal / External
RX Power	-23 to -3	dBm	±3dB	Internal / External

Digital Diagnostic Memory Map

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.





Pin Definitions

Pin Diagram

20	VeeT	1 VeeT
19	TD-	2 TxFault
18	TD+	3 Tx Disable
17	VeeT	4 MOD-DEF(2)
16	VccT	5 MOD-DEF(1)
15	VccR	6 MOD-DEF(0)
14	VeeR	7 Rate Select
13	RD+	8 Los
12	RD-	9 VeeR
11	VeeR	10 VeeR
	Top of Board	Bottom of Board (as viewed thru top of board)



Pin Descriptions

Pin Descri	iptions			
Pin	Signal Name	Description	Plug Seq.	Notes
1	V_{EET}	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	MOD_DEF(2)	SDA Serial Data Signal	3	Note 3
5	MOD_DEF(1)	SCL Serial Clock Signal	3	Note 3
6	MOD_DEF(0)	TTL Low	3	Note 3
7	Rate Select	Not Connected	3	
8	LOS	Loss of Signal	3	Note 4
9	V_{EER}	Receiver ground	1	
10	V_{EER}	Receiver ground	1	
11	V_{EER}	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 5
13	RD+	Received Data Out	3	Note 5
14	V_{EER}	Receiver ground	1	
15	V_{CCR}	Receiver Power Supply	2	
16	V_{CCT}	Transmitter Power Supply	2	
17	V_{EET}	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 6
19	TD-	Inv. Transmit Data In	3	Note 6
20	V _{EET}	Transmitter Ground	1	

Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

- 1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) TX Disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7k~10kΩ resistor. Its states are:

Low (0 to 0.8V): Transmitter on (>0.8V, < 2.0V): Undefined

High (2.0 to 3.465V): Transmitter Disabled Open: Transmitter Disabled

- 3) Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7k~10kΩ resistor on the host board. The pull-up voltage shall be VccT or VccR.
 - Mod-Def 0 is grounded by the module to indicate that the module is present

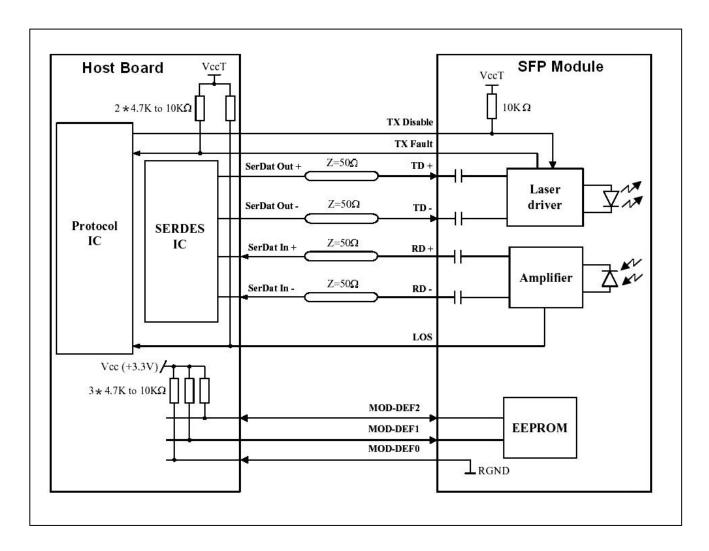
Mod-Def 1 is the clock line of two wire serial interface for serial ID

Mod-Def 2 is the data line of two wire serial interface for serial ID

- 4) LOS is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor. Pull up voltage between 2.0V and Vcc+0.3V. Logic 1 indicates loss of signal; Logic 0 indicates normal operation. In the low state, the output will be pulled to less than 0.8V.
- 5) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 6) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

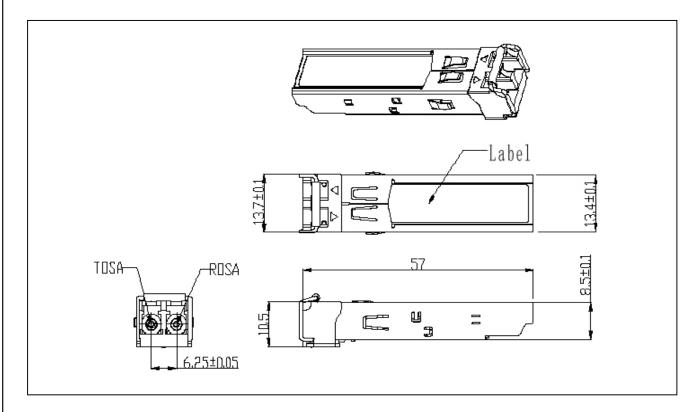


Recommended Interface Circuit





Mechanical Dimensions



Regulatory Compliance

SFP transceiver is designed to be Class I Laser safety compliant and is certified per the following standards

Feature	Agency	Standard	Certificate / Comments
Laser Safety	FDA	CDRH 21 CFR 1040 annd Laser Notice No. 50	1120294-000
Product Safety	BST	EN 60825-1: 2007 EN 60825-2: 2004 EN 60950-1: 2006	BT0905142002
Environmental protection	SGS	RoHS Directive 2002/95/EC	GZ0902008346/CHEM
EMC	CCIC	EN 55022: 2006+A1: 2007 EN 55024: 1998+A1: 2001+A2: 2003	CTE09050018



Ordering information

Part Number	Product Description
SNS SFP-GDCWXDËXX	1.25Gbps.80km.ÁnÔYÖTÁFIÍ€{ dFÎF€{ ÊD°C~+70°C.ÁÙØÚÁ/¦æ}•&^ãc^¦Á

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References

- 1. Small Form Factor Pluggable (SFP) Transceiver Multi-Source Agreement (MSA), September 2000.
- 2. Telcordia GR-253and ITU-T G.957 Specifications.

Important Notice

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