2Gbps 850nm Multimode Dual Receiver
Small Form Factor

Features

- Dual Receiver with LC Receptacle
- Small Form Factor 2 x 5 Footprint Metal Case
- AC Coupled and LVPECL Data Output
- TTL Signal Detect Output
- Single 3.3V Power Supply

Specifications

Parameter	Symbol	Min.	Typ.	Max.	Unit
Data Rate (NRZ)	B	-	2	-	Gb/s
Optical Input (avg.) Sensitivity (1) (3)	P_{IN}	-	-	-15	dBm
Saturation	P_S	0	-	-	dBm
Optical Wavelength	λ	830	850	870	nm
Output Rise Time (10-90%)	t_r	-	-	0.15	ns
Output Fall Time (10-90%)	t_f	-	-	0.15	ns
Data Output (4)	V_{OL} V_{OH}	V_{CC} -1.840 V_{CC} -1.045	- -	V_{CC} -1.62 V_{CC} -0.88	V V
Signal Detect Asserted (avg)	P_A	-	-	-17	dBm
Signal Detect Deasserted (avg)	P_D	-25	-	-	dBm
Hysteresis	Hys	-	2	-	dB
Supply Voltage	V_{CC}	3.1	3.3	3.5	V
Supply Current	I_{CC}	-	-	100	mA

Note:

(1) With 0.275 NA, 62.5/125μm multimode fiber.
(2) Eye mask diagram is compliant to FC-PI-2 Eye Diagram..
(3) 2^7 -1 PRBS, BER= 10^{-12}.
(4) Compatible LVPECL logic levels.

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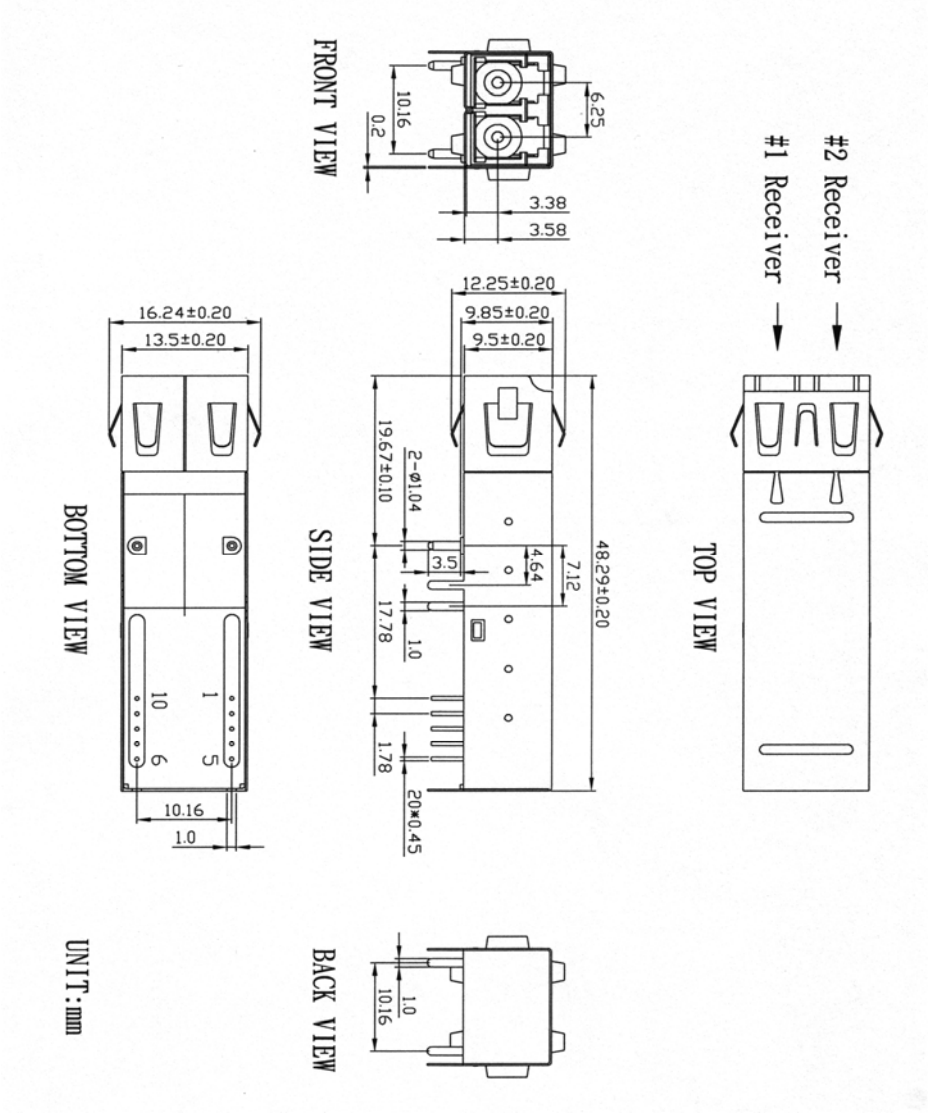
Absolute Maximum Ratings

Parameter		Min.	Max.	Unit
Operating Temperature	-1	-10	70	°C
	-2	-40	85	°C
Storage Temperature		-40	100	°C
Lead Soldering Limits		-	240/10	°C /sec
Supply Voltage		-0.2	4	V

Ordering Information

SNS-RD85MM4-1VLC3ARXF1G

Outline Drawing



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Pin-out Description

Pin No.	Pin Name	Description
1	#1 V_{EER}	#1 Receiver Ground
2	#1 V_{CCR}	#1 Receiver Power Supply
3	#1 SD	#1 Receiver Signal Detect
4	#1 $\overline{RD}$	#1 Receiver Data Out(Inverted)
5	#1 *RD*	#1 Receiver Data Out
6	#2 *RD*	#2 Receiver Data Out
7	#2 $\overline{RD}$	#2 Receiver Data Out(Inverted)
8	#2 SD	#2 Receiver Signal Detect
9	#2 V_{CCR}	#2 Receiver Power Supply
10	#2 V_{EER}	#2 Receiver Ground