Multi-mode 1.25Gbps 1310nm 2X5 SFF Transceiver

Features

- LC duplex receptacle
- Standard 2 x 5 footprint
- 1310nm LD transmitter
- AC or DC coupled LVPECL/PECL compatible data input and output
- Transmitter disable input
- PECL or TTL signal detect output
- Single 3.3V or 5V power supply

Specifications

Parameter		Symbol	Min.	Typ.	Max.	Unit
Transmitter						
Data Rate (NRZ)		B	-	1250	-	Mb/s
Optical Output Power (avg.) (1) (2) (4)		P_o	-8	-	-	dBm
Extinction Ratio		ER	8.3	-	-	dB
Optical Wavelength		λ_C	1270	1310	1355	nm
Spectral Width		$\Delta\lambda$	-	-	2.5	nm
Output Rise Time (10-90%)		t_r	-	0.3	0.7	ns
Output Fall Time (10-90%)		t_f	-	0.3	0.7	ns
Data Input (9)	DC Coupled	V_{IL} V_{IH}	V_{CC} -1.810 V_{CC} -1.165	- -	V_{CC} -1.475 V_{CC} -0.880	V V
	AC Coupled (Differential)	V_I	0.25	-	1.6	V
Tx Disable Input		V_{DIL} V_{DIH}	0 2	- -	0.8 V_{CC}	V
Supply Voltage		V_{CC}	3.10 4.75	3.3 5.0	3.50 5.25	V V
Supply Current		I_{CC}	-	-	110	mA
Receiver						
Data Rate (NRZ)		B	-	1250	-	Mb/s
Optical Input (avg.) Sensitivity (1) (4)		P_{IN}	-	-	-20	dBm
Optical Wavelength		λ	1100	-	1600	nm
Output Rise Time (10-90%)		t_r	-	-	0.4	ns
Output Fall Time (10-90%)		t_f	-	-	0.4	ns
Data Output (9)	DC Coupled	V_{OL} V_{OH}	V_{CC} -1.840 V_{CC} -1.045	- -	V_{CC} -1.62 V_{CC} -0.88	V V
	AC Coupled (Differential)	V_I	0.6	-	1.8	V
Signal Detect Asserted (avg.)		P_A	-	-	-20	dBm
Signal Detect Deasserted (avg.)		P_D	-25	-	-	dBm
Hysteresis		P_{HYS}	-	3	-	dB
Supply Voltage		V_{CC}	3.10 4.75	3.3 5.0	3.50 5.25	V
Supply Current		I_{CC}	-	-	100	mA

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Notes：

(1) With 0.275 NA, 62.5/125μm fiber.
(2) Driven with a differential signal.
(3) Transmitter eye mask diagram is compliant to IEEE802.3z Eye Diagram.
(4) 2^7 -1 PRBS, BER= 10^{-12}.
(5) Compatible with LVPECL and PECL logic levels.
(6) The transmitter output should not be viewed directly.

Absolute Maximum Ratings

Parameter		Min.	Max.	Unit
Operating Temperature	-1	0	70	°C
	-2	-40	85	°C
Storage Temperature		-40	100	°C
Lead Soldering Limits		-	240/10	°C /sec
Supply Voltage	5V	-0.2	7	V
	3.3V	-0.2	4	V

Ordering Information

SNS-T R 1 3 M M 3 - 1 L L C □ A R 1 □ □

Operating Temperature Range：
1：0 ~ 70°C
2：-40 ~ 85°C

Data Coupling & SD Output Level：

Symbol	Tx Coupling	Rx Coupling	SD
C	AC	DC	PECL
D	AC	DC	TTL
E	AC	AC	PECL
F	AC	AC	TTL
G	DC	DC	PECL
H	DC	DC	TTL
I	DC	AC	PECL
J	DC	AC	TTL

Supply Voltage：
5：5V
3：3.3V

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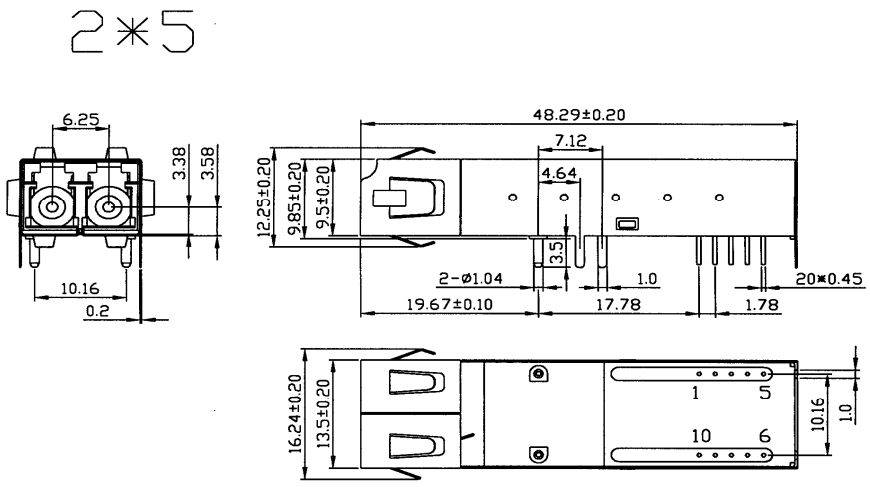
Outline Drawing



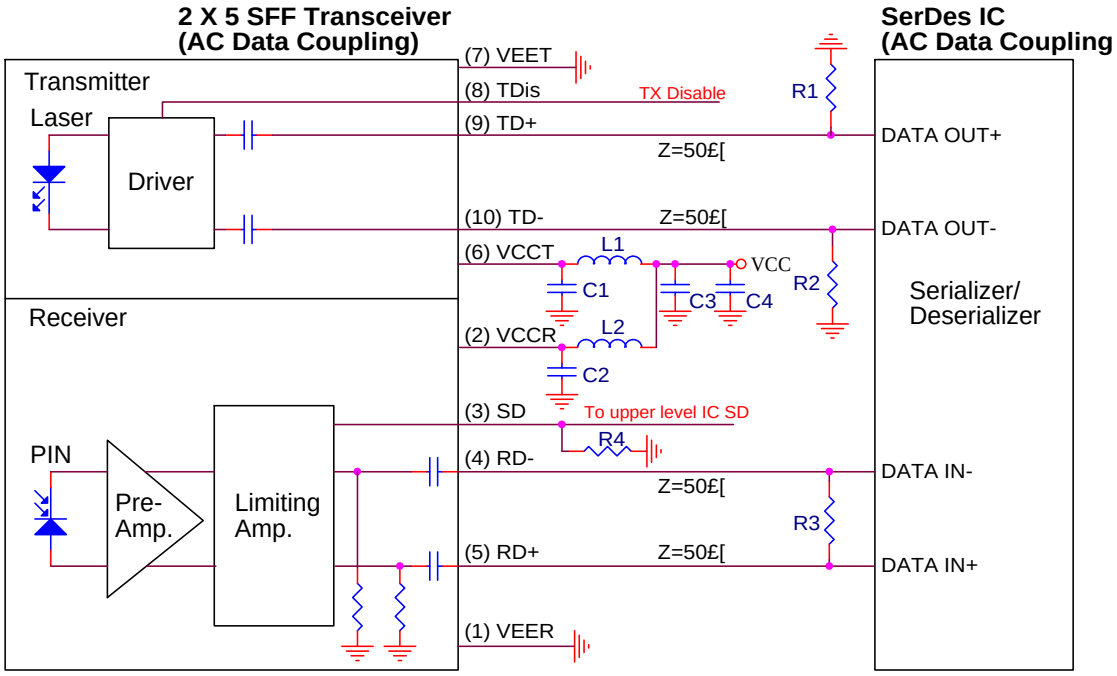
UNIT：mm

Pinout Description

Pin No.	Symbol	Description
1	V_{EER}	Receiver Ground
2	V_{CCR}	Receiver Power Supply (5V/3.3V)
3	SD	Receiver Signal Detect
4	RD-	Receiver Data Out (Inverted)
5	RD+	Receiver Data Out
6	V_{CCT}	Transmitter Power Supply (5V/3.3V)
7	V_{EET}	Transmitter Ground
8	TDis	Input Logic Low Level to Switch Transmitter “ON” Input Logic High Level to Switch Transmitter “OFF”
9	TD+	Transmitter Data in
10	TD-	Transmitter Data In (Inverted)

Application Notes

Recommended AC Coupling Interface Circuit：



L1=L2=1£gH or ferrite bead
C1=C2=C3=0.1£gF
C4=10£gF
R1, R2, R3 depends on SerDes IC specification.
(Consult the SerDes IC application information)
R4=510£[

NOTE¡G
1. Transmission line characteristic impedance Z=50£[.
2. R1, R2, R3 as close to SerDes IC as possible.

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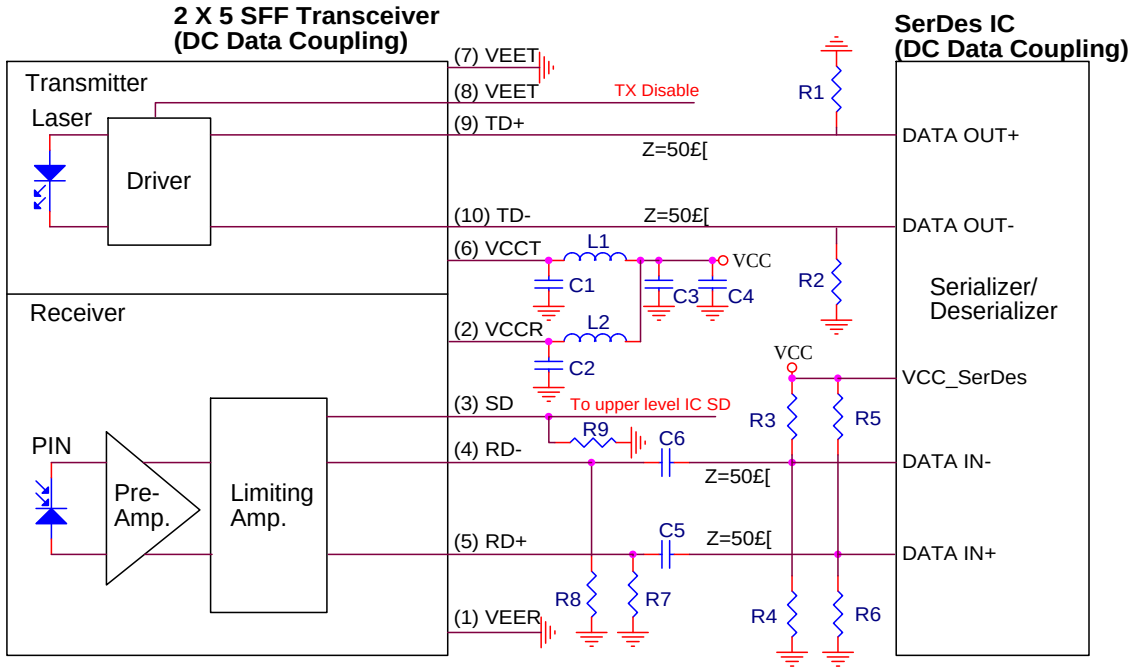
Recommended DC Coupling Interface Circuit：



L1=L2=1£gH or ferrite bead
C1=C2=C3=C5=C6=0.1£gF
C4=10£gF
R1, R2, R3, R4, R5, R6 depends on SerDes IC specification.
(Consult the SerDes IC application information)
R7=R8=270£[(VCC=3.3V)
=510£[(VCC=5V)
R9=510£[

NOTE¡G
1. Transmission line characteristic impedance Z=50£[.
2. R1, R2, R3, R4, R5, R6 as close to SerDes IC as possibl
3. R7, R8 as close to 2X5 Transceiver as possible.